# Digital Logic Design: a rigorous approach © Chapter 4: Directed Graphs



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Book Homepage: http://www.eng.tau.ac.il/~guy/Even-Medina In the following definition we consider a directed acyclic graph G = (V, E) with a single sink called the root.

### Definition

A DAG G = (V, E) is a rooted tree if it satisfies the following conditions:

- There is a single sink in G.
- For every vertex in V that is not a sink, the out-degree equals one.

The single sink in rooted tree G is called the root, and we denote the root of G by r(G).







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#### Theorem

In a rooted tree there is a unique path from every vertex to the root.

G=(V, E) rooted tree => VV I path V many root proof by ind. on IVI. base: |V|=1, trivial. hyp: holds if IV = n step: prove for MI=n+1. G DAG  $\Rightarrow$   $\exists$  source  $\vee$   $\{V' \stackrel{*}{=} V \setminus \{v\}\}$ consider G' = (V', E') where  $\{E' \stackrel{*}{=} E \setminus E_V\}$ G' is a routed tree: degout (u) is unchanged ind hyp on G': YueV' I! path u path foot what about  $v^{?}$  degat  $(v) = 1 \Rightarrow \exists u : (v, u) \in E$ 



2nd proof: 1) Epath to root  
2) Unique path to root  
Epath to root:  
pick VEV. build path recursively  
as follows:  
Vo 
$$\leftarrow$$
 V  
if Vi Sink stop.  
if Vi Zink, EU: (Vi, U) EE.  
set Vin  $\leftarrow$  U.  
Since [path] <  $\infty$ , alg. must terminate.  
Bink is Unique, path reaches the root.





## composition & decomposition of rooted trees



Figure: A decomposition of a rooted tree G into two rooted trees  $G_1$  and  $G_2$ .

- each the rooted tree  $G_i = (V_i, E_i)$  is called a tree hanging from r(G).
- Leaf : a source node.
- interior vertex : a vertex that is not a leaf.
- parent : if  $u \rightarrow v$ , then v is the parent of u.
- Typically maximum in-degree= 2.

- The rooted trees hanging from r(G) are ordered. Important in parse trees.
- Arcs are oriented from the leaves towards the root. Useful for modeling circuits:
  - leaves = inputs
  - root = output of the circuit.

